

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Kazunori ANAZAWA et al.

Application No.: New U.S. Patent Application

Filed: December 1, 2003

Docket No.: 117883

For: ACTIVE ELECTRONIC DEVICE AND ELECTRONIC APPARATUS

INFORMATION DISCLOSURE STATEMENT

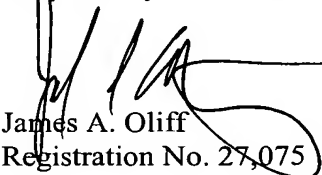
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 CFR §1.56, the attention of the Patent and Trademark Office is hereby directed to the reference(s) listed on the attached PTO-1449. Unless otherwise indicated herein, one copy of each reference is attached. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the reference(s) be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

- ☒ 1. This Information Disclosure Statement is being filed (a) within three months of the U.S. filing date of this non-CPA application, OR (b) before the mailing date of a first Office Action on the merits in the present application. No certification or fee is required.
- ☒ 2. Relevance of the references is discussed in the present specification.

Respectfully submitted,


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Form PTO-1449 (REV. 8-83)		US Dept. of Commerce PATENT & TRADEMARK OFFICE		ATTY DOCKET NO. 117883		APPLICATION NO. New U.S. Patent Application	
INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)				APPLICANTS Kazunori ANAZAWA et al.			
				FILING DATE December 1, 2003			

U.S. PATENT DOCUMENTS						
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS

FOREIGN PATENT DOCUMENTS						
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)		
		S. J. Tans et al., "Room-temperature transistor based on a single carbon nanotube", Nature, vol. 393, pp. 49-51, 1998
		A. Javey et al., "Carbon Nanotube Transistor Arrays for Multistage Complementary Logic and Ring Oscillators", Nano Letters, vol. 2, pp. 929-939, 2002
		P.G. Collins et al., "Engineering Carbon Nanotubes and Nanotube Circuits Using Electrical Breakdown", vol. 292, pp. 706-709

EXAMINER	DATE CONSIDERED
Examiner: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

Date: December 1, 2003